

FIG. 3A

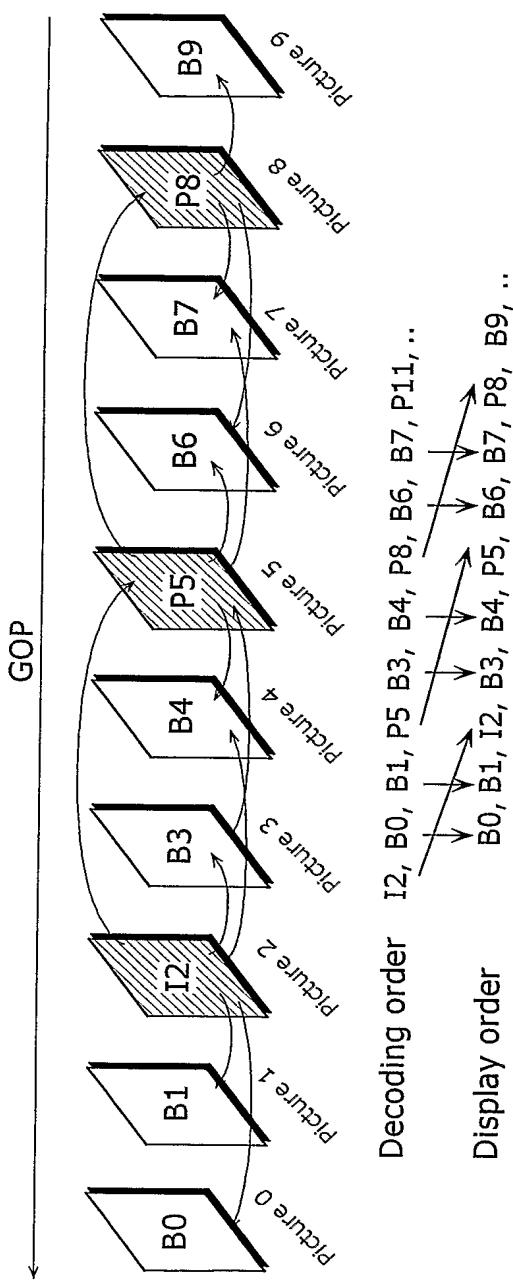


FIG. 3B

FIG. 4

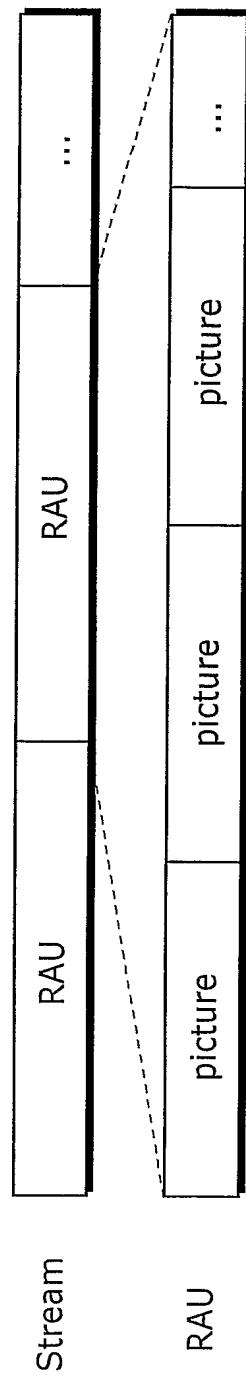
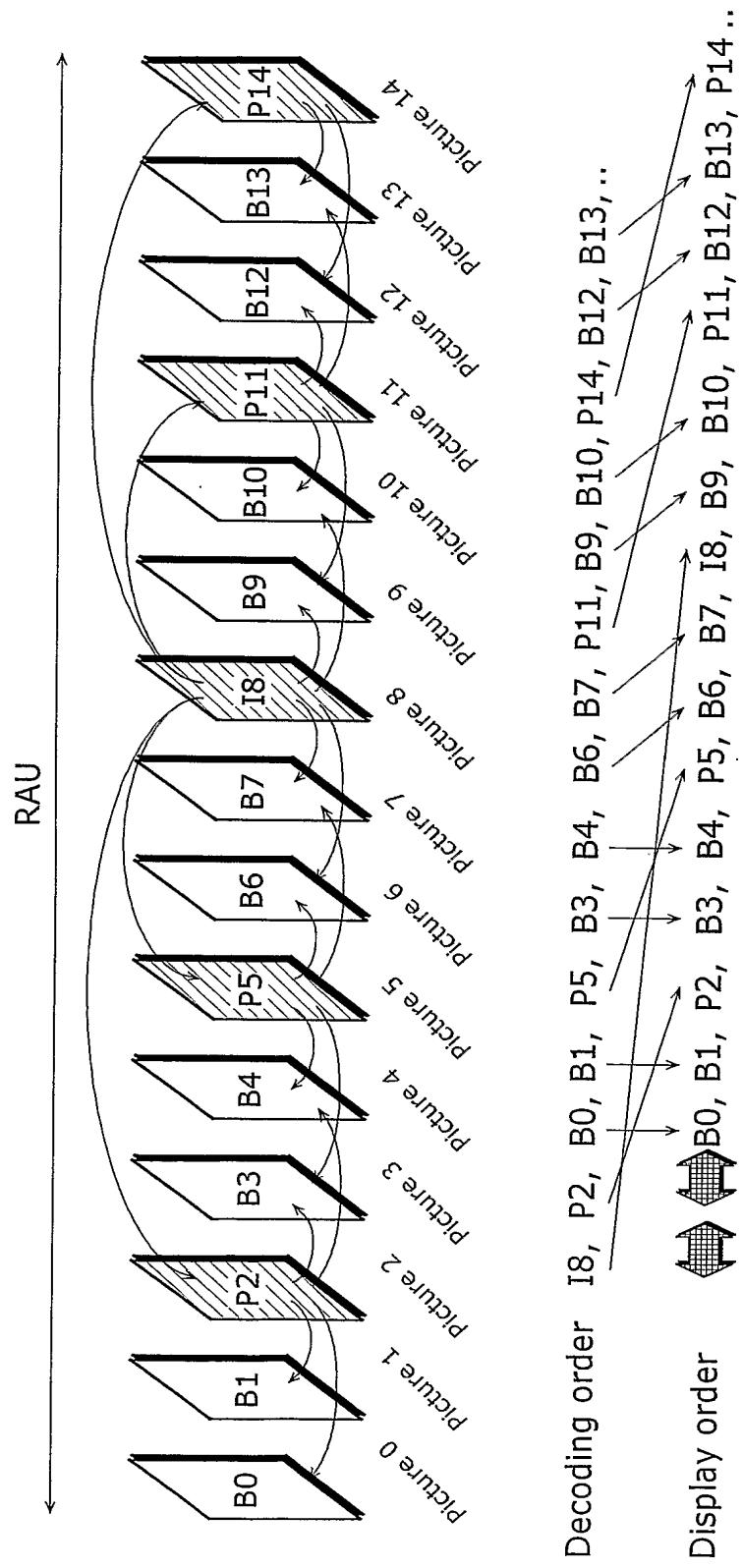


FIG. 5



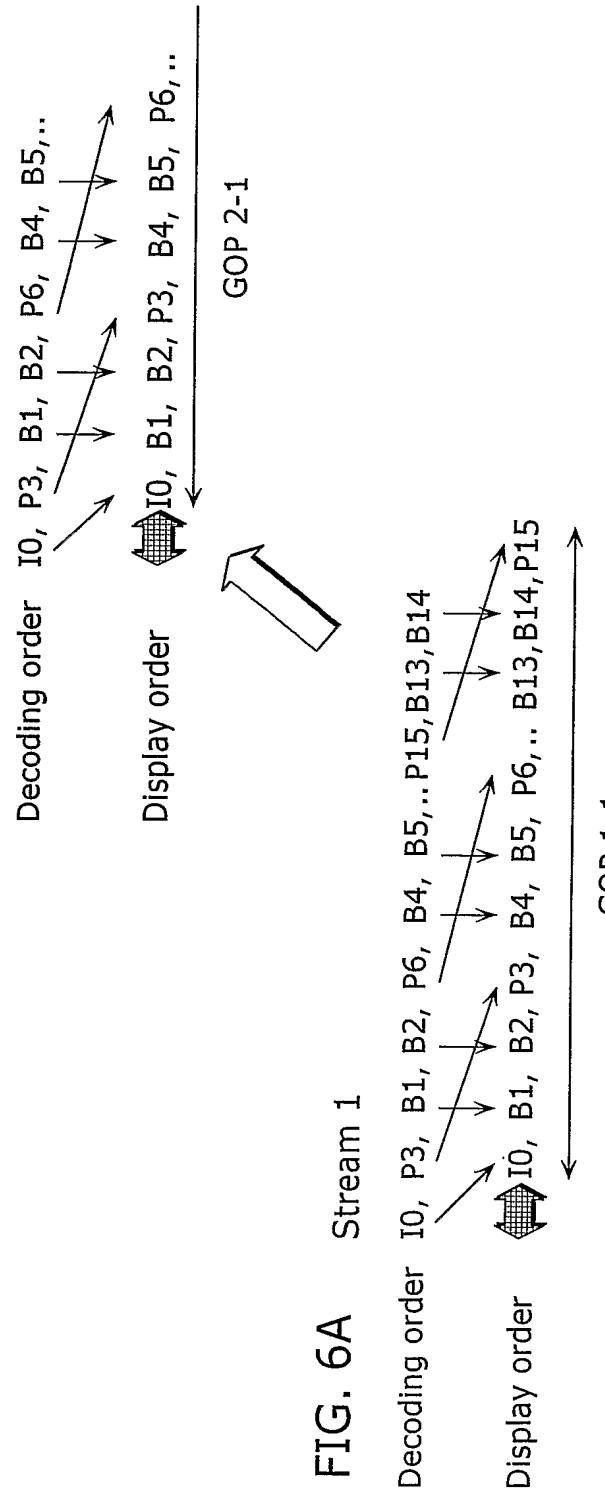
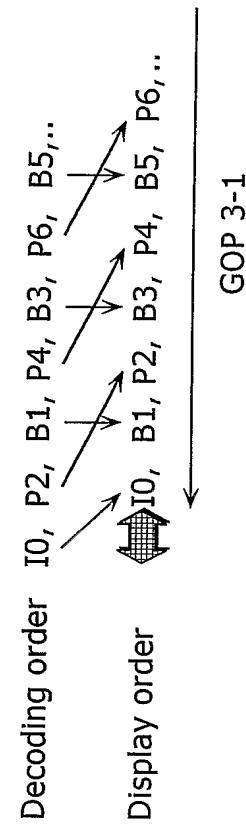
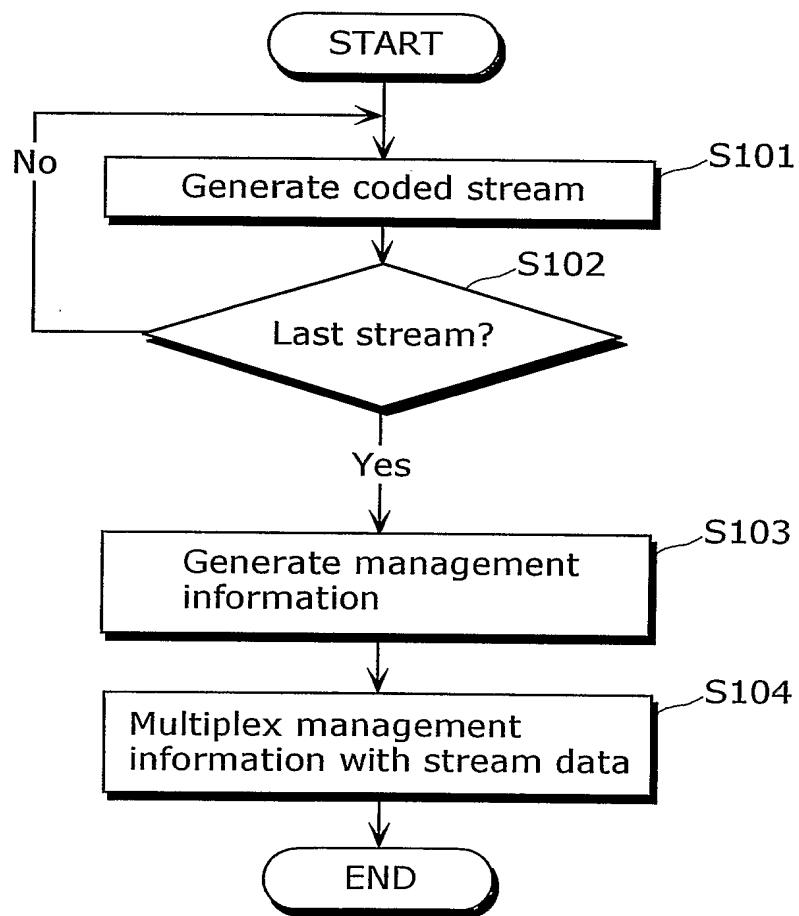
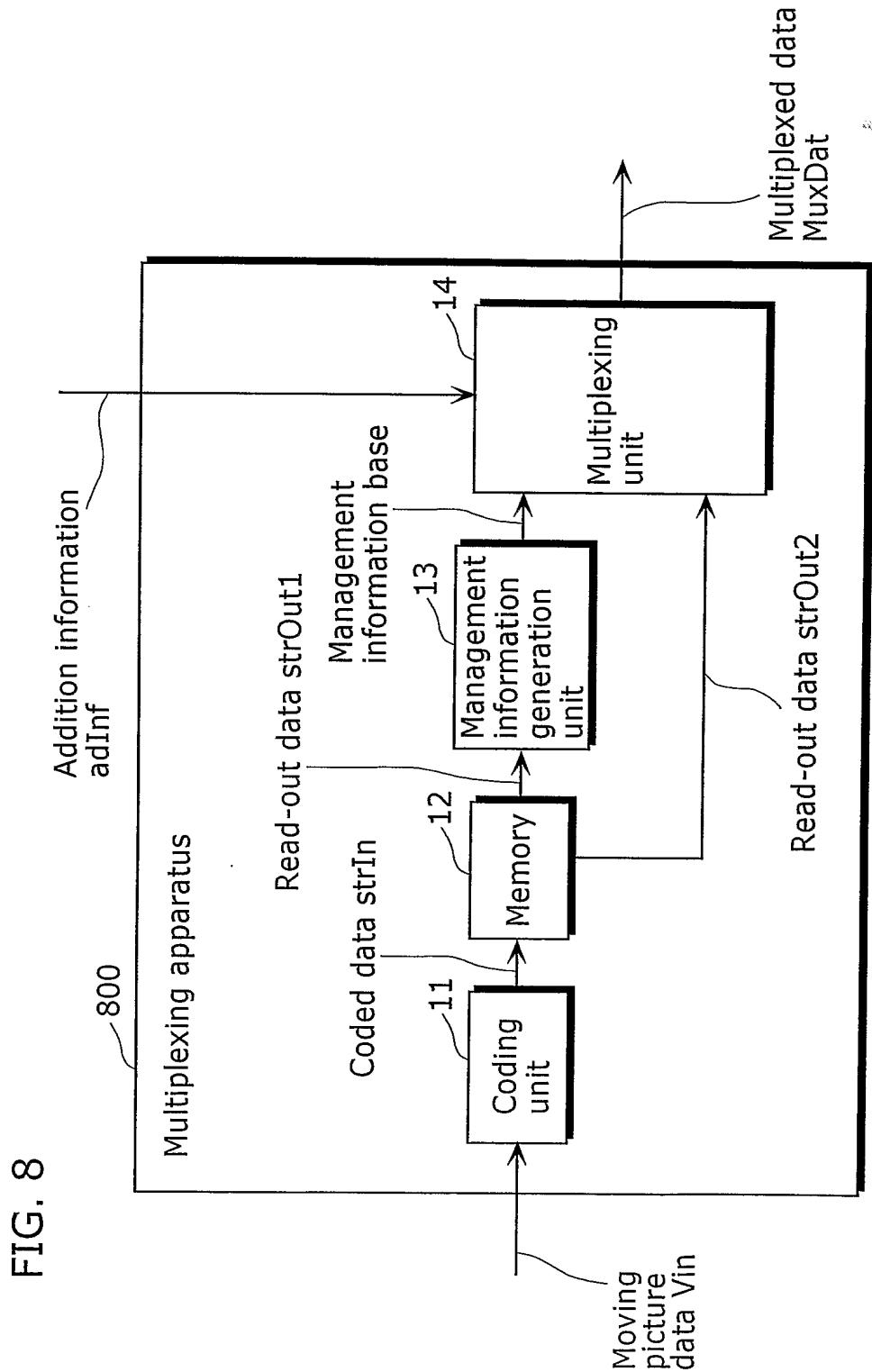
**FIG. 6B Stream 2****FIG. 6C Stream 3**

FIG. 7





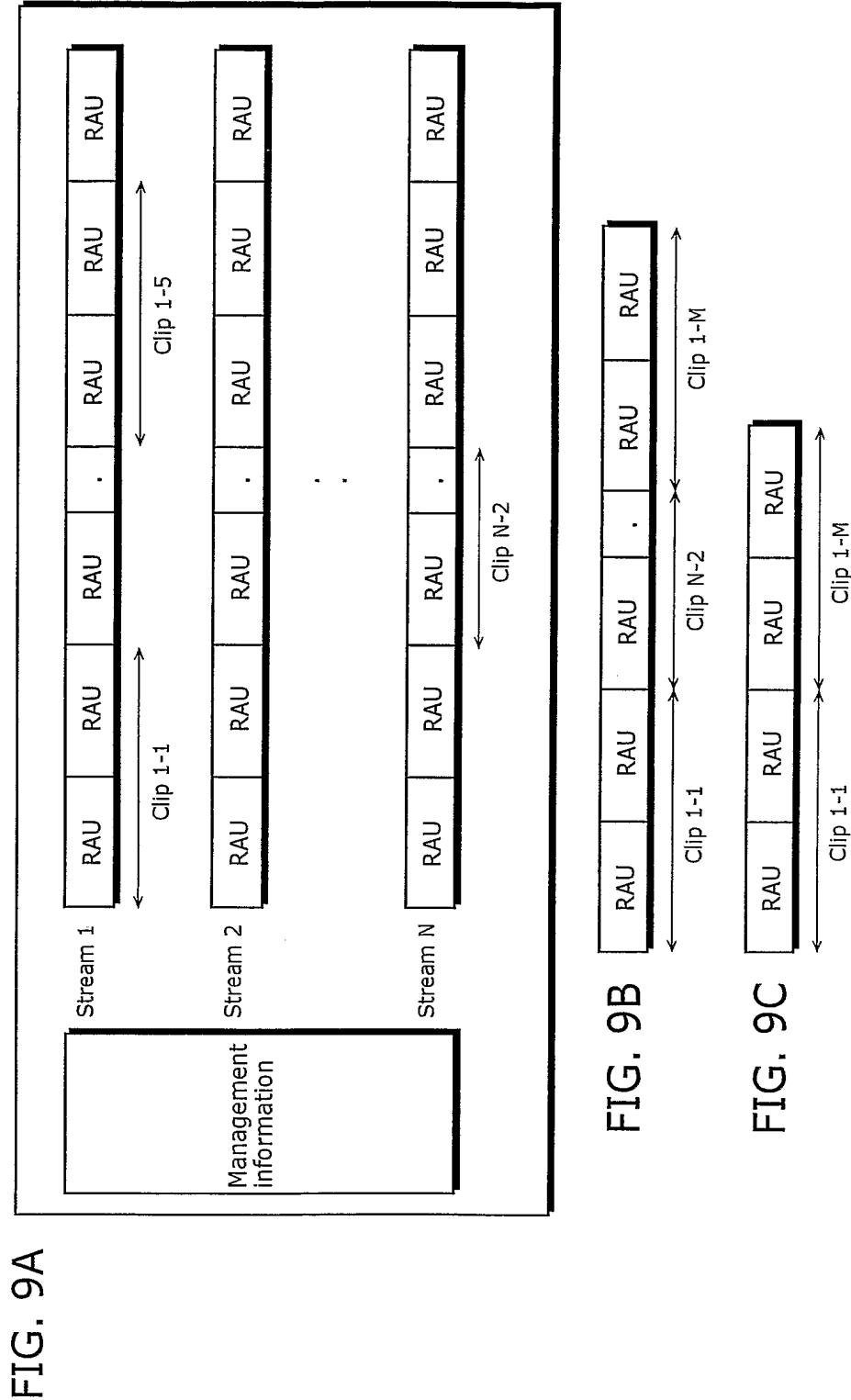


FIG. 10

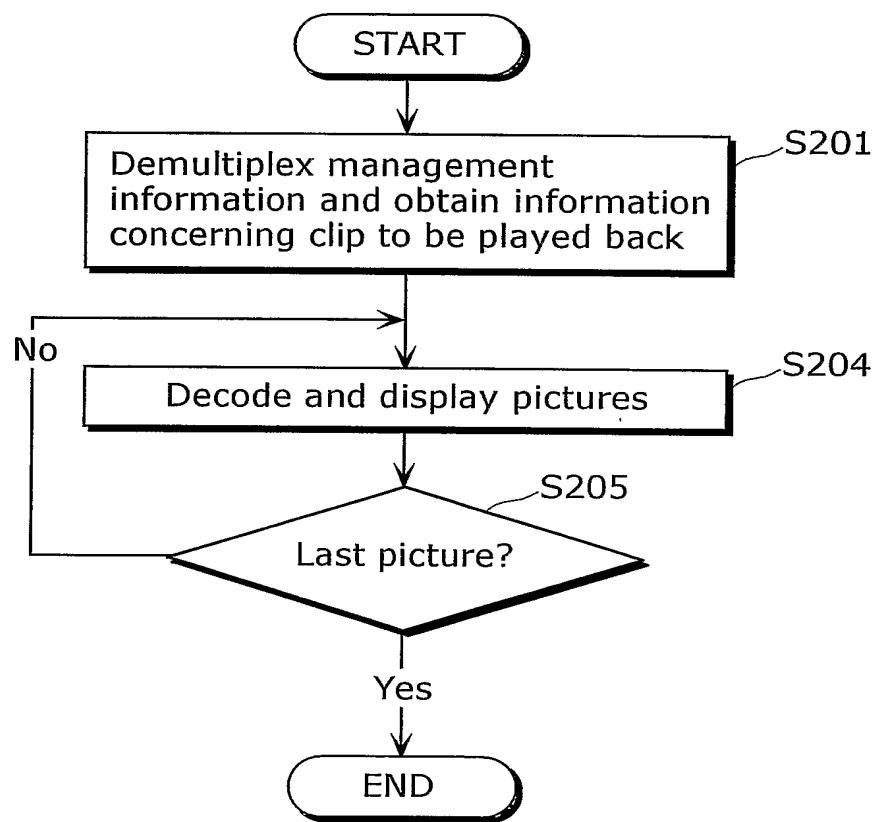
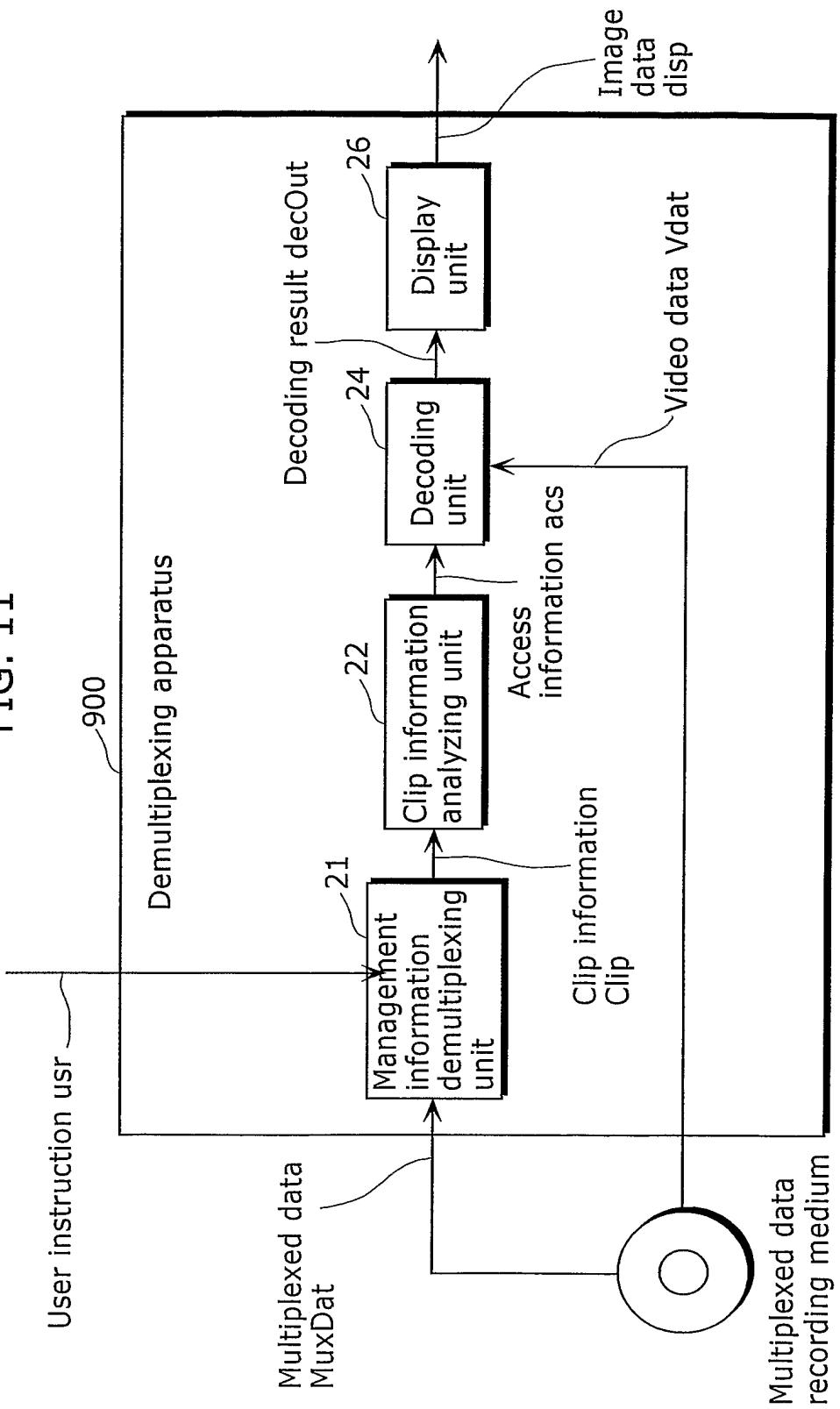


FIG. 11



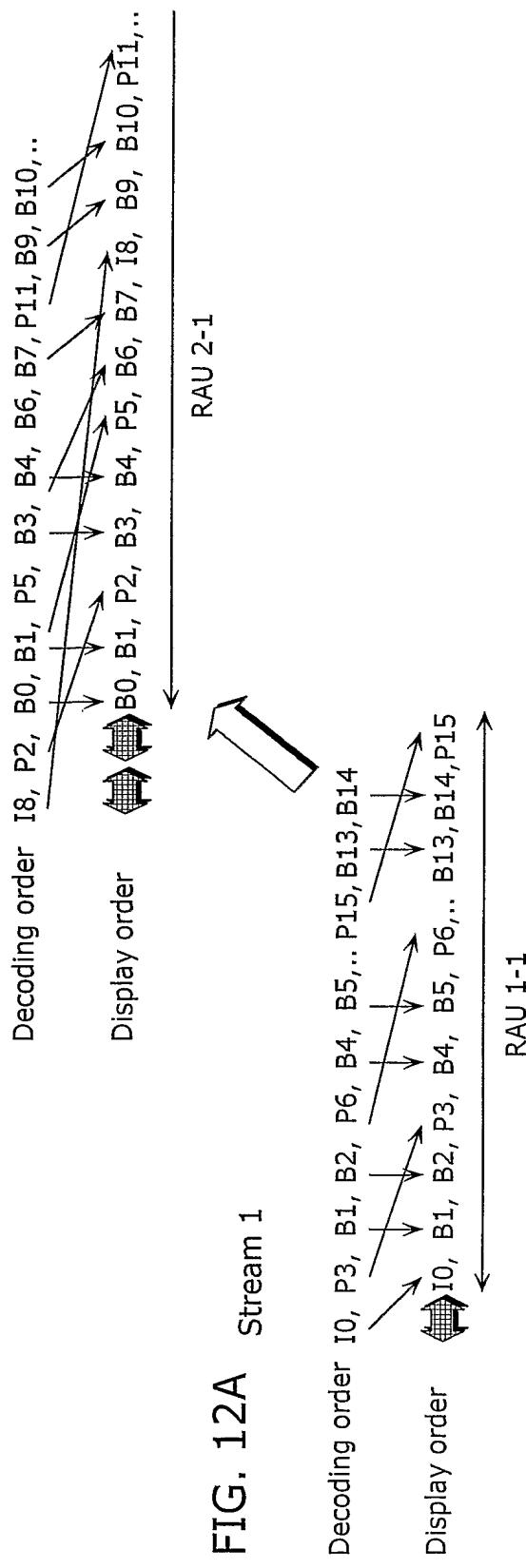
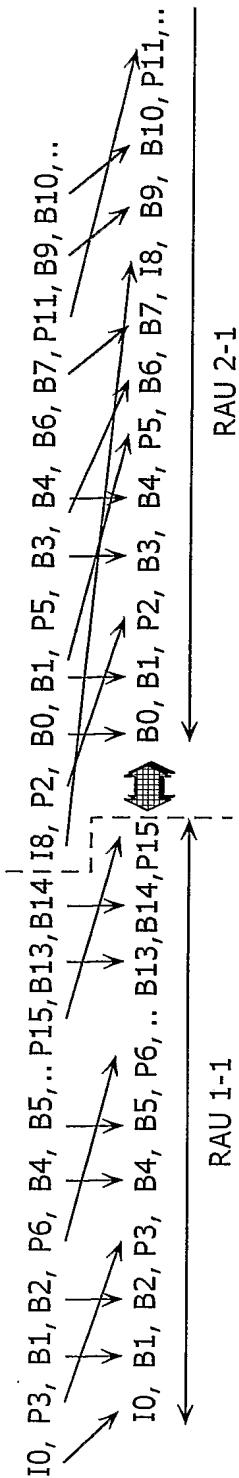
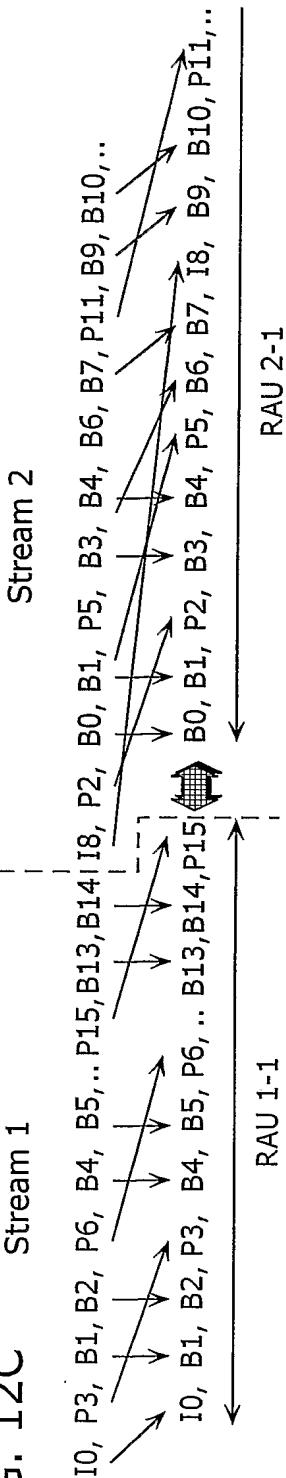
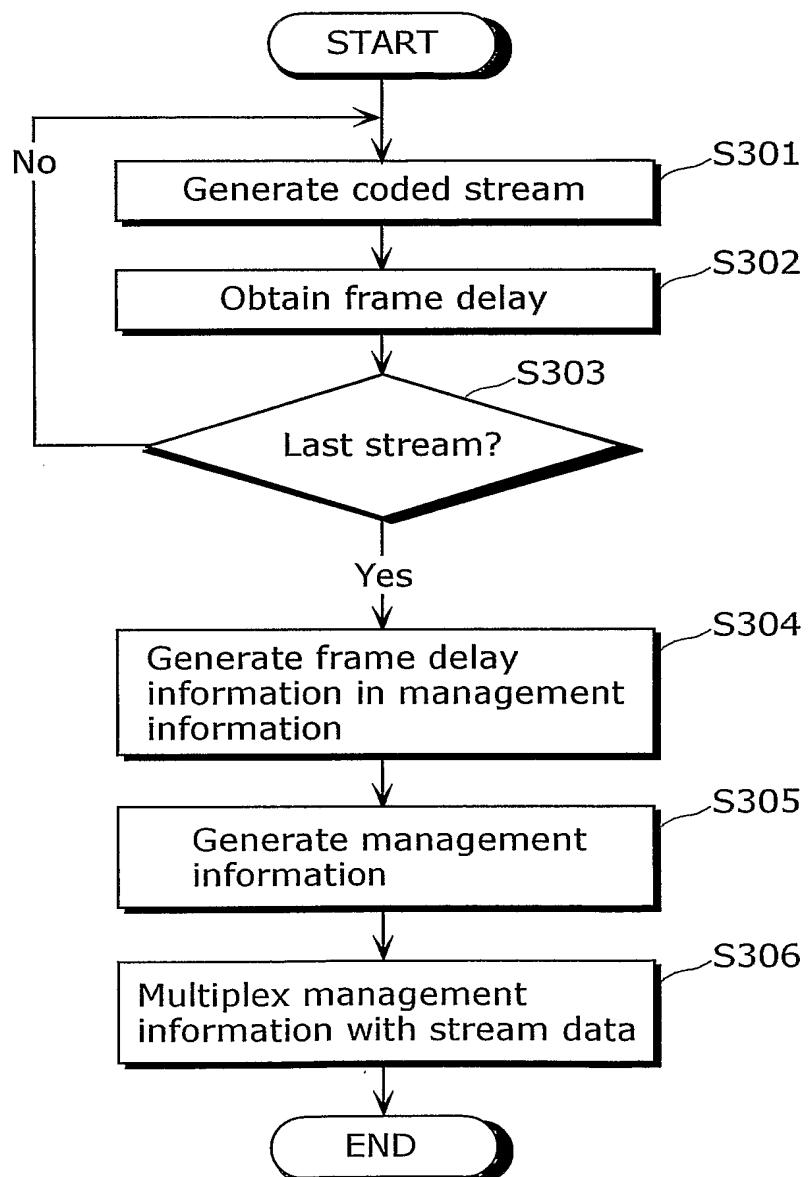
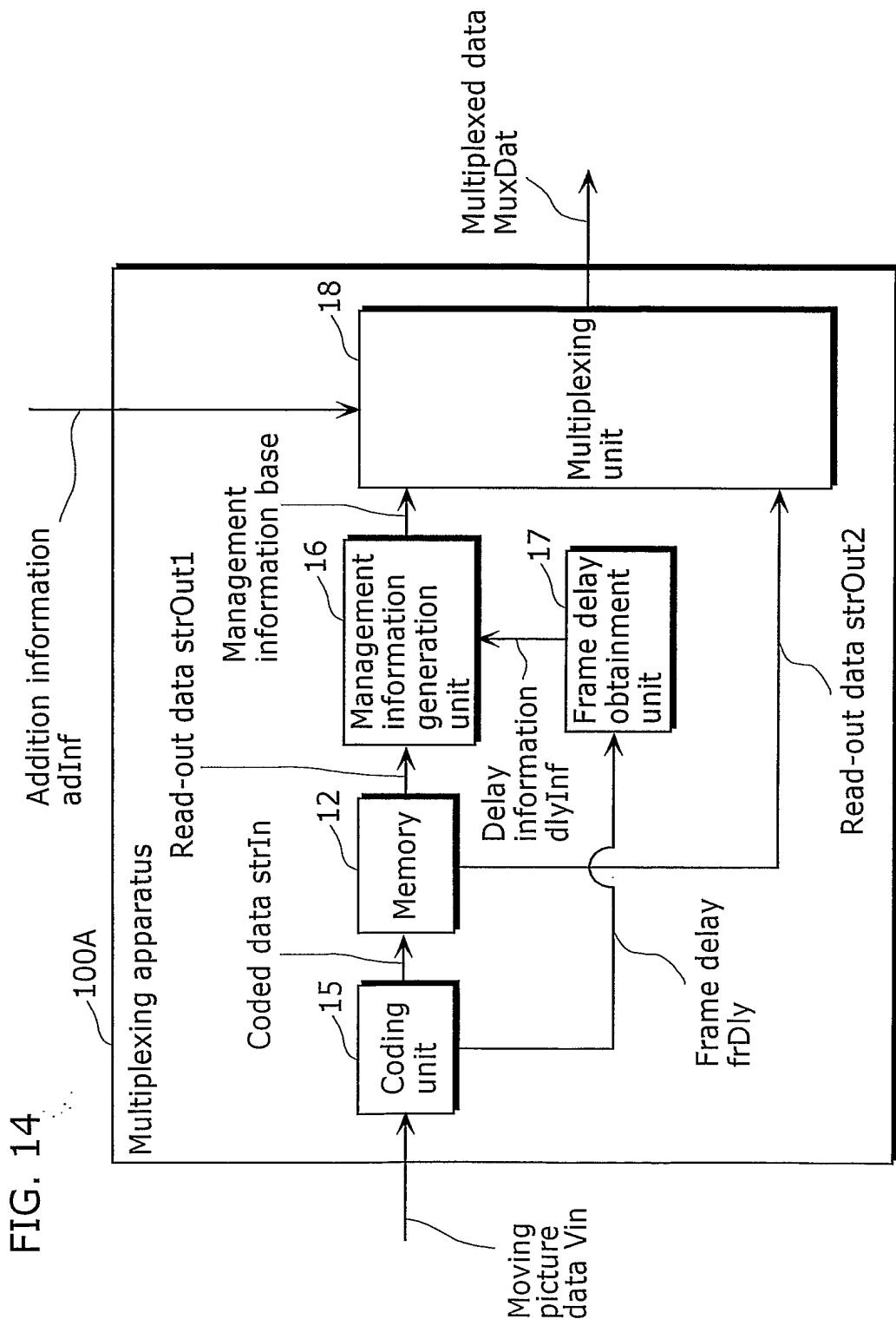
**FIG. 12B Stream 2****FIG. 12C Stream 1****FIG. 12C Stream 2**

FIG. 13





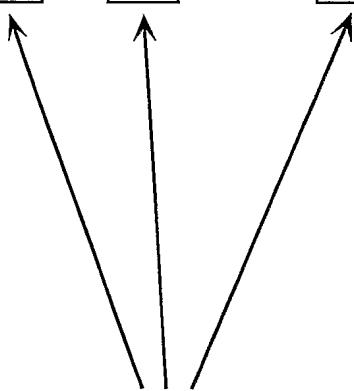
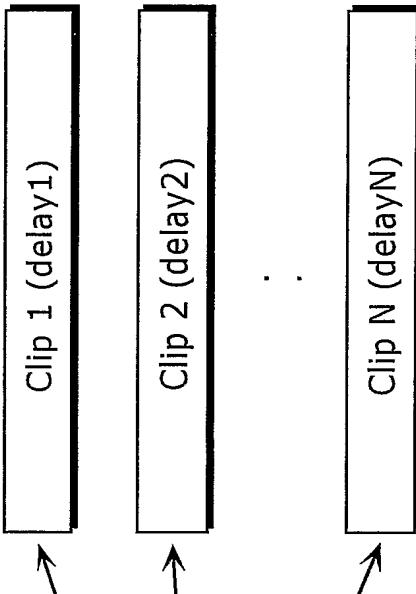


FIG. 15A

Delay table	
delay1	
delay2	
..	
delayN	

FIG. 15B

FIG. 15D Value of frame delay (Common)

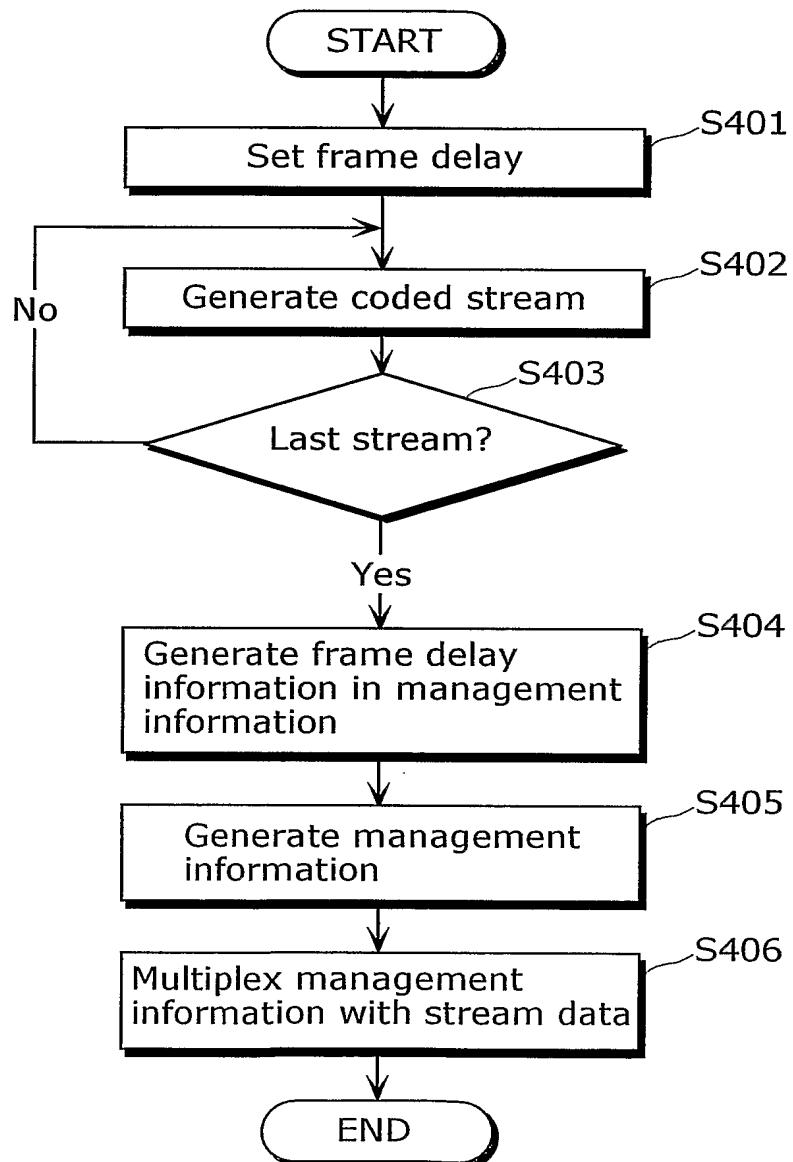
Maximum value of  
answer obtained  
from delay1-N

FIG. 15C

Information indicating whether frame delays are  
equal among clips

FIG. 15E

FIG. 16



**FIG. 17**  
Multiplexing apparatus  
100B

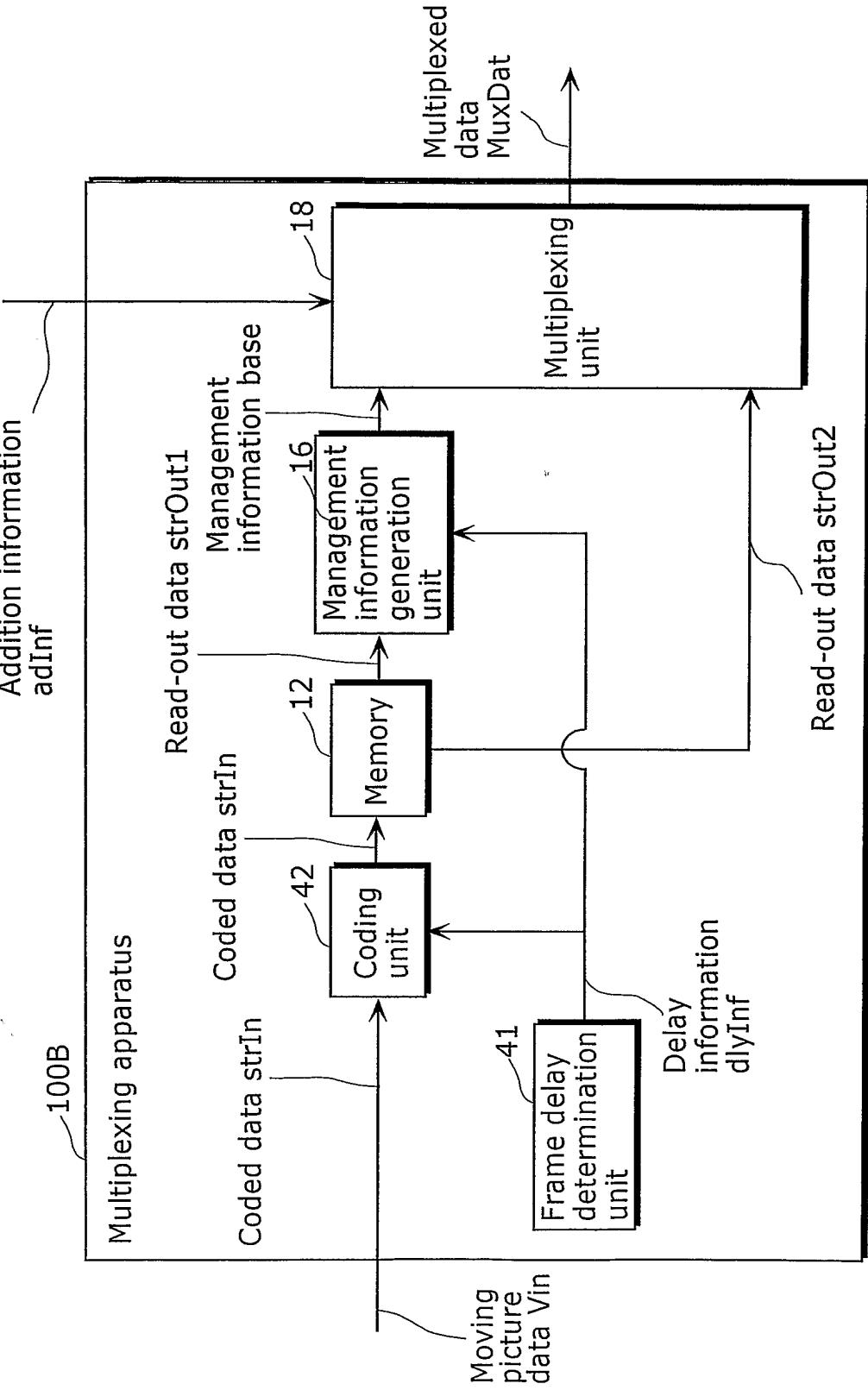


FIG. 18

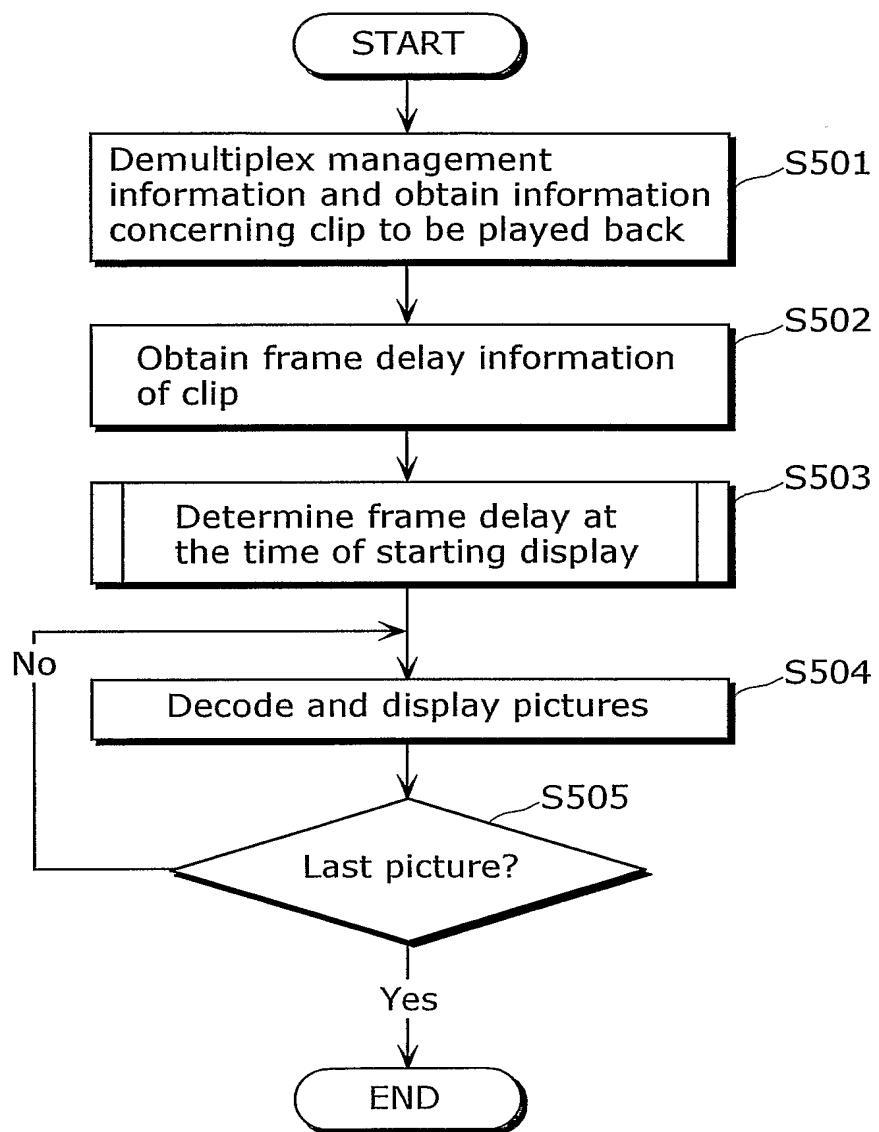
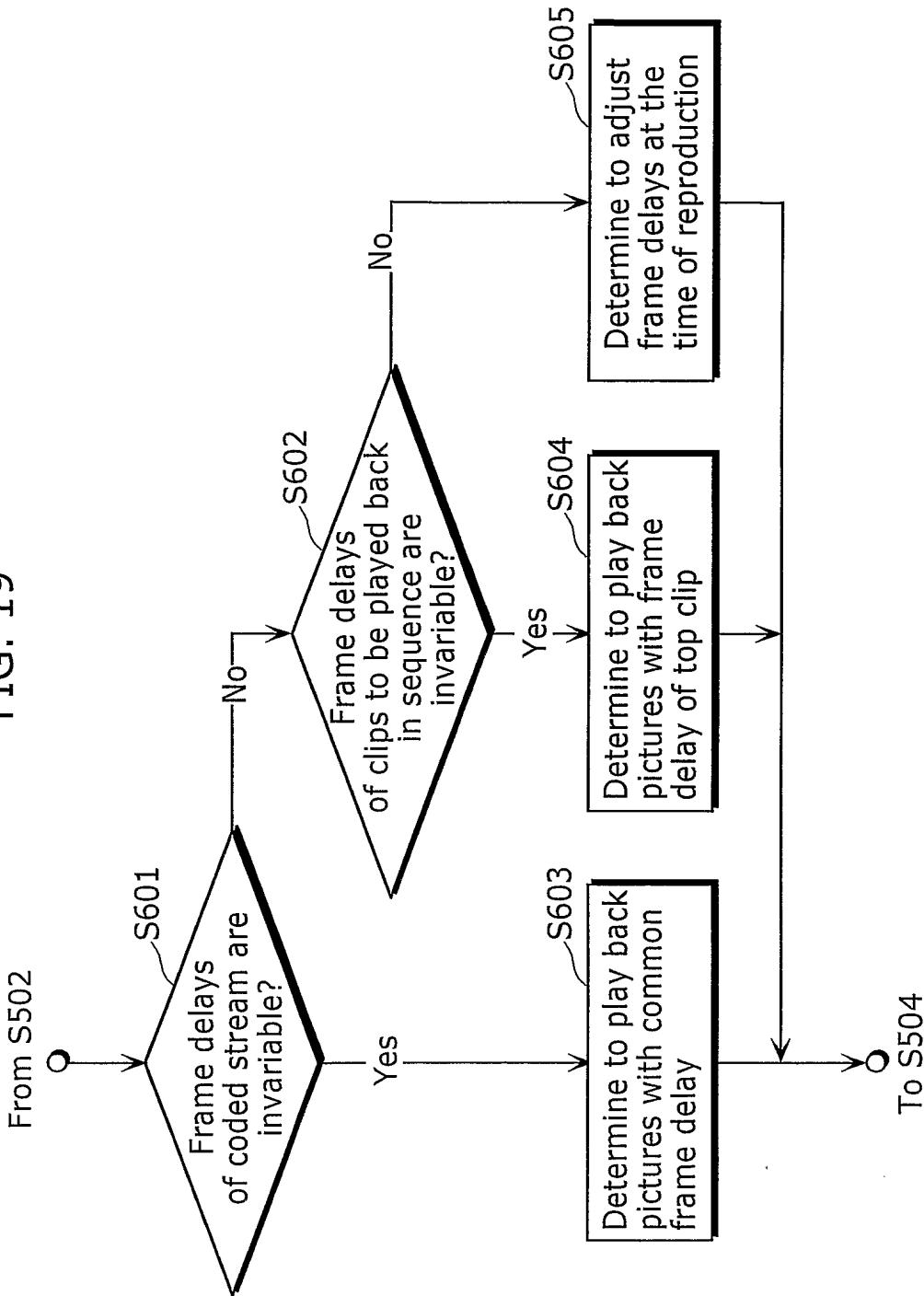


FIG. 19



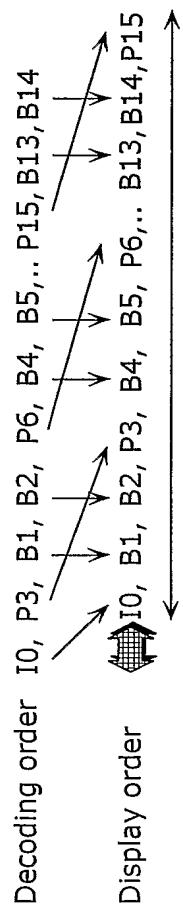
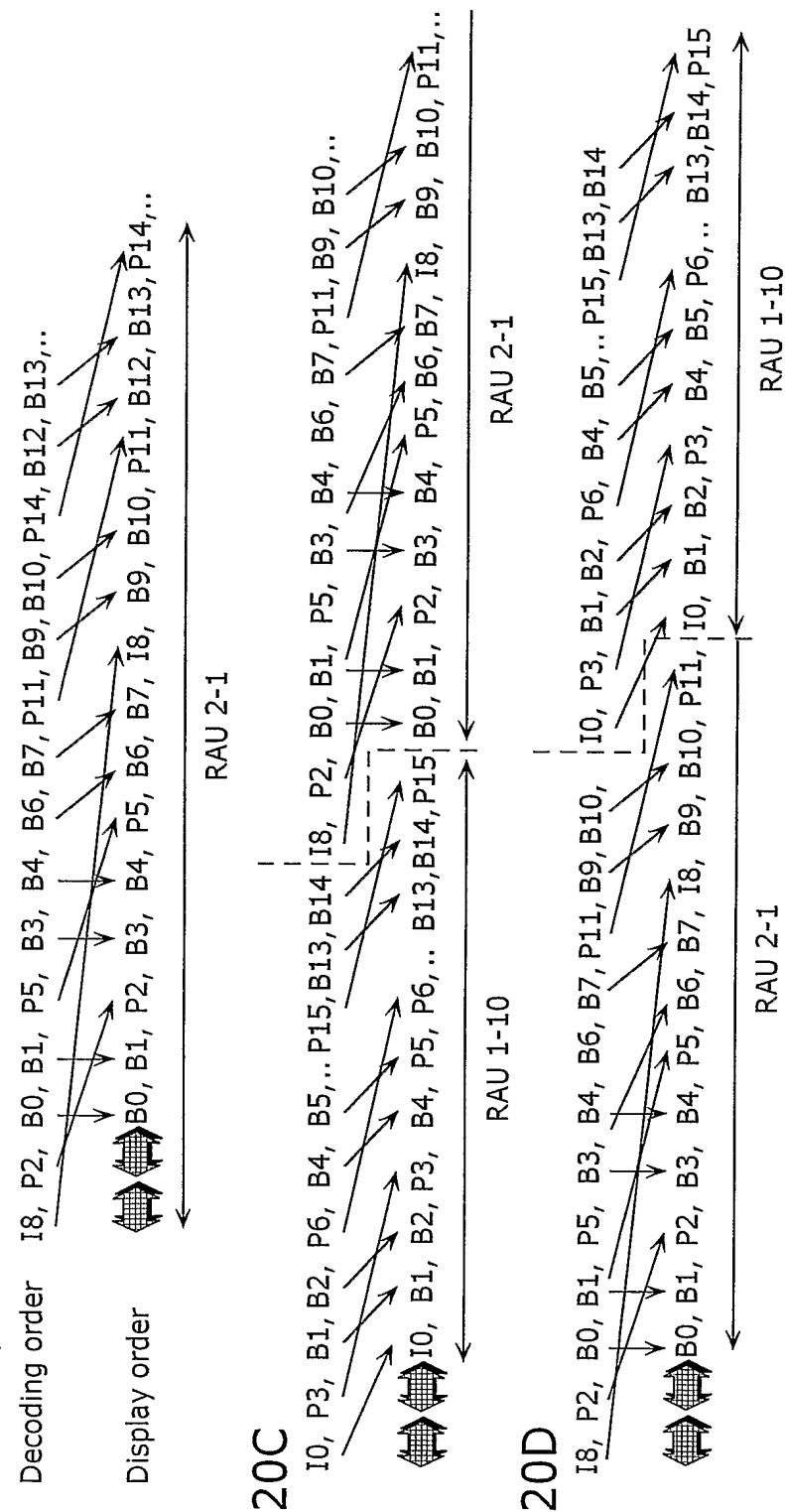
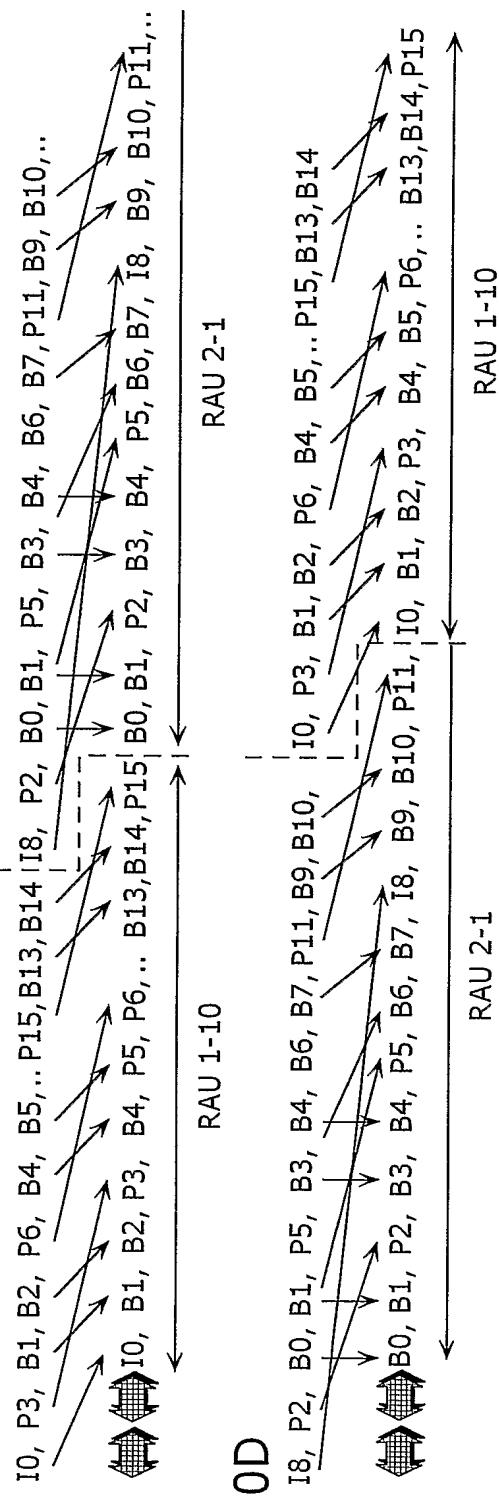
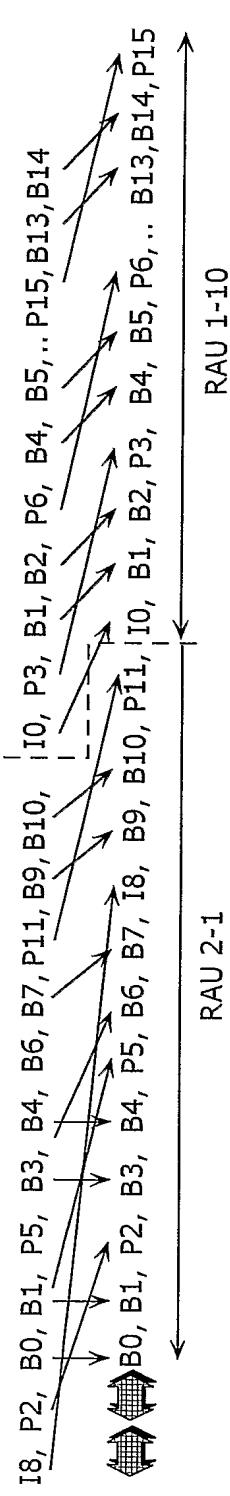
**FIG. 20A Clip 1****FIG. 20B Clip 2****FIG. 20C****FIG. 20D**

FIG. 21

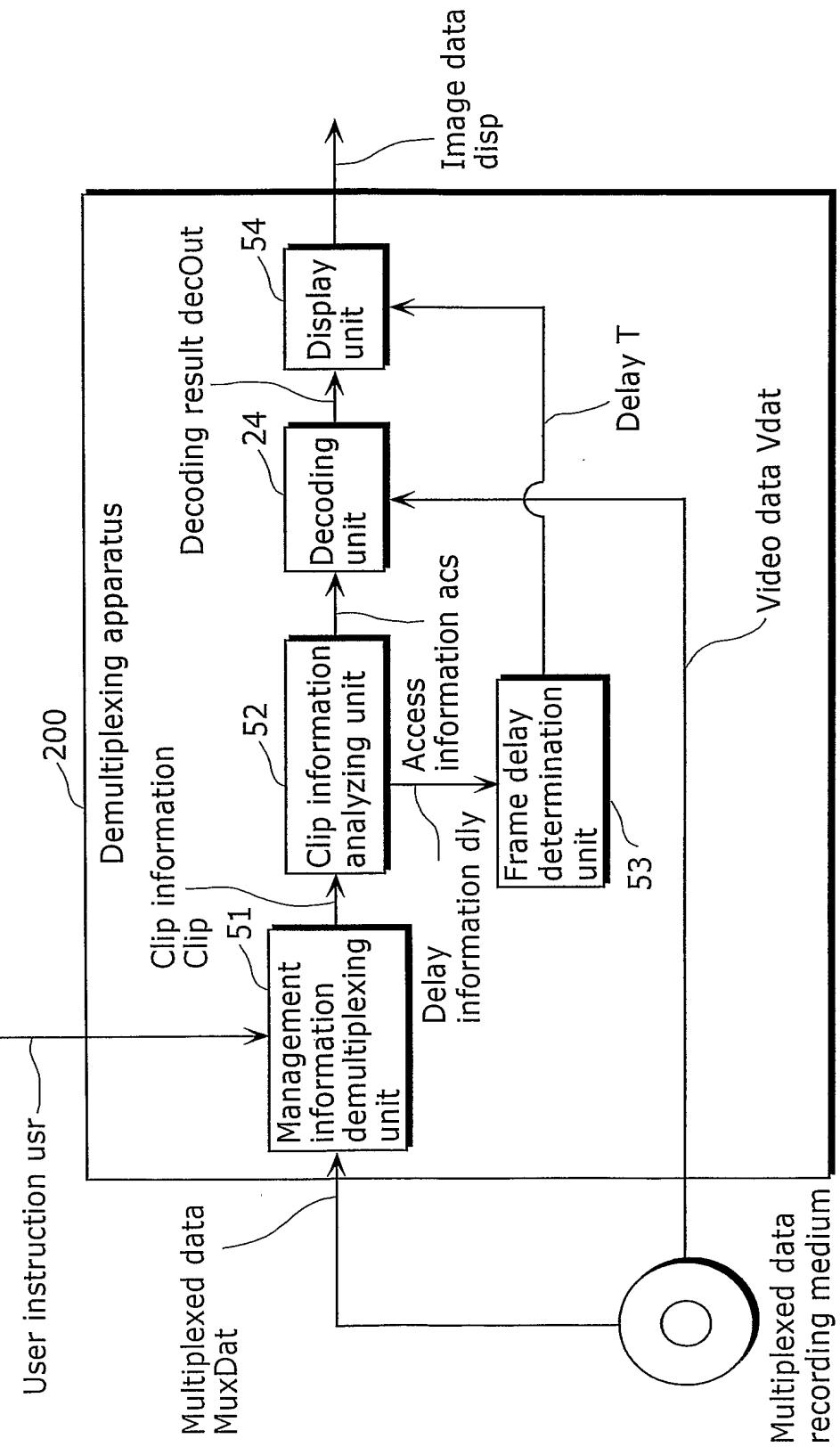


FIG. 22



FIG. 23

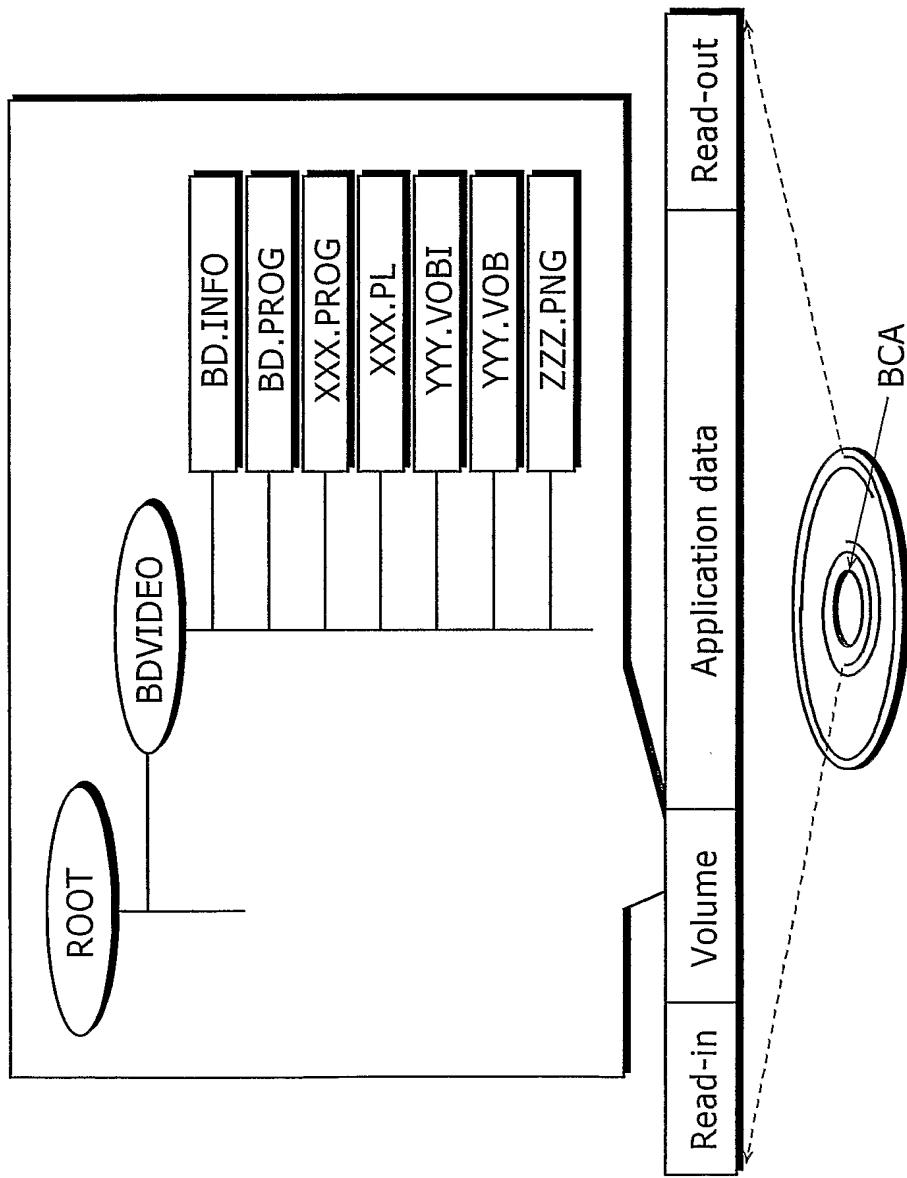


FIG. 24

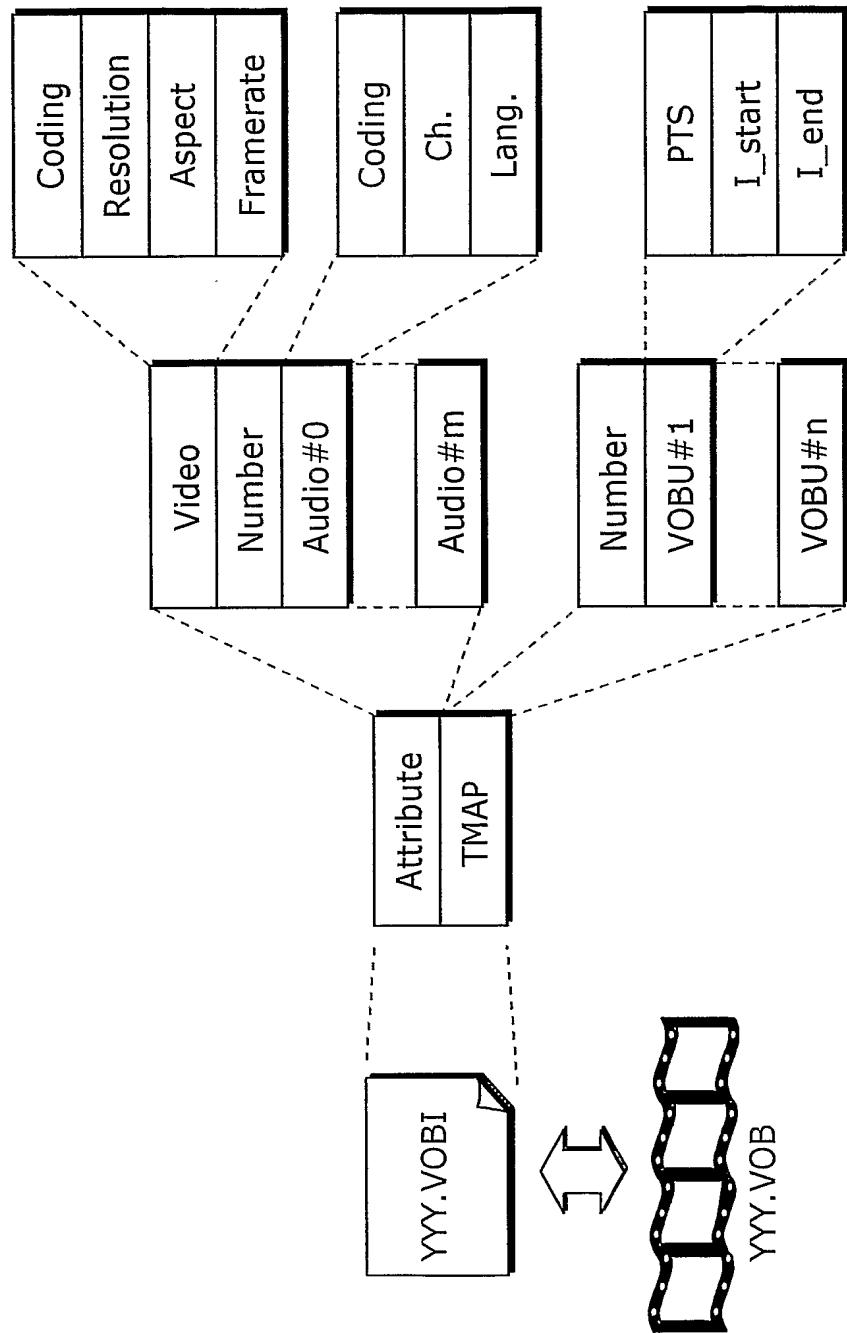
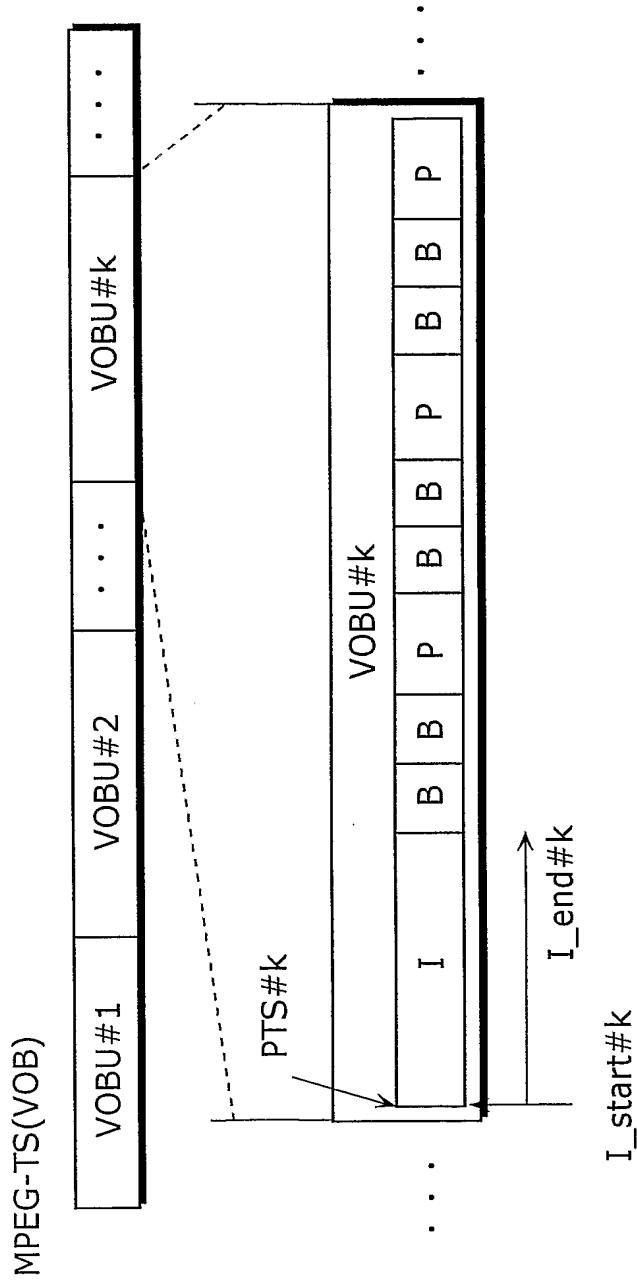


FIG. 25



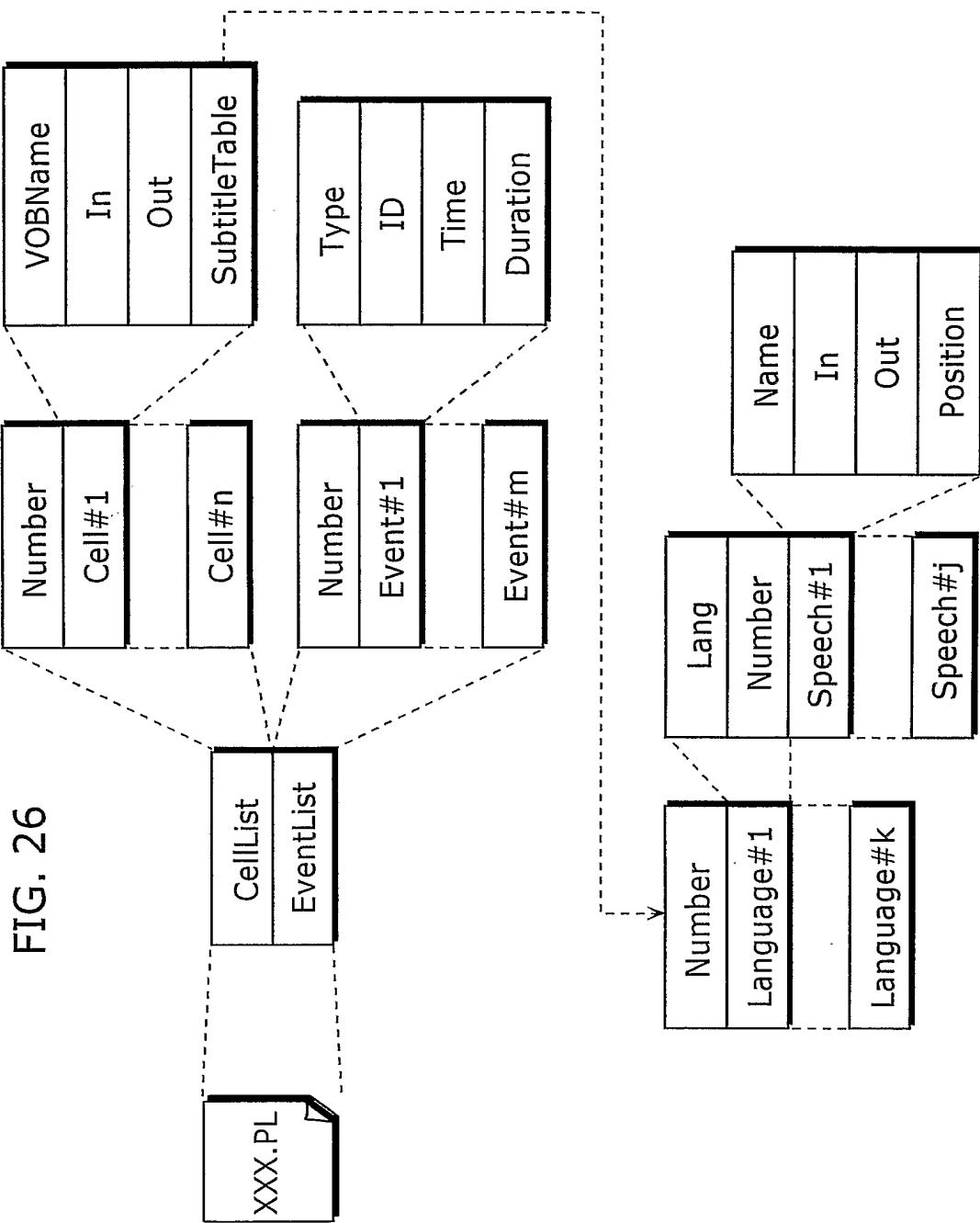


FIG. 27

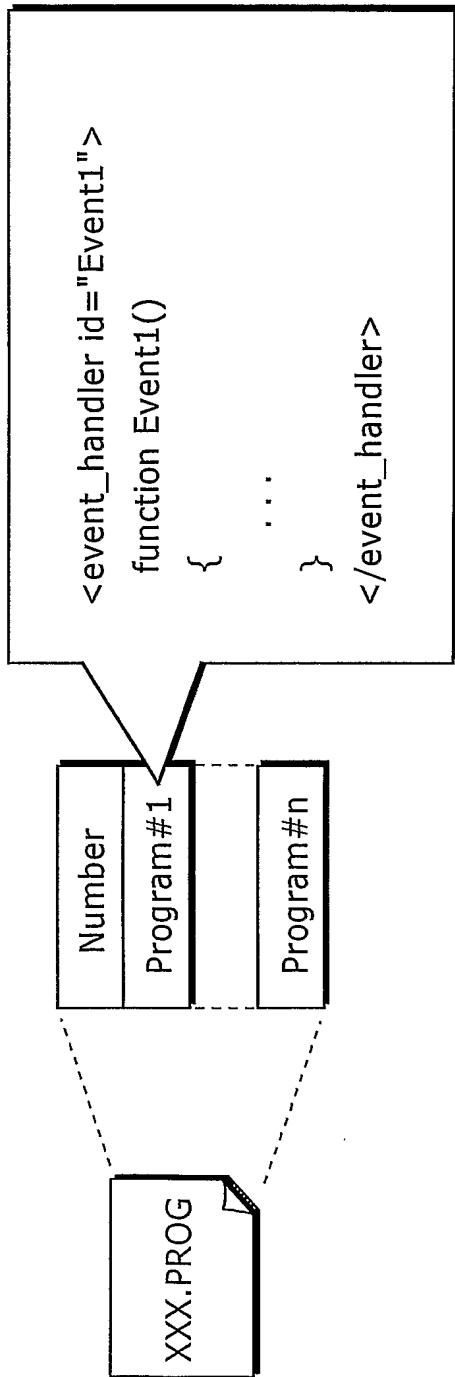


FIG. 28

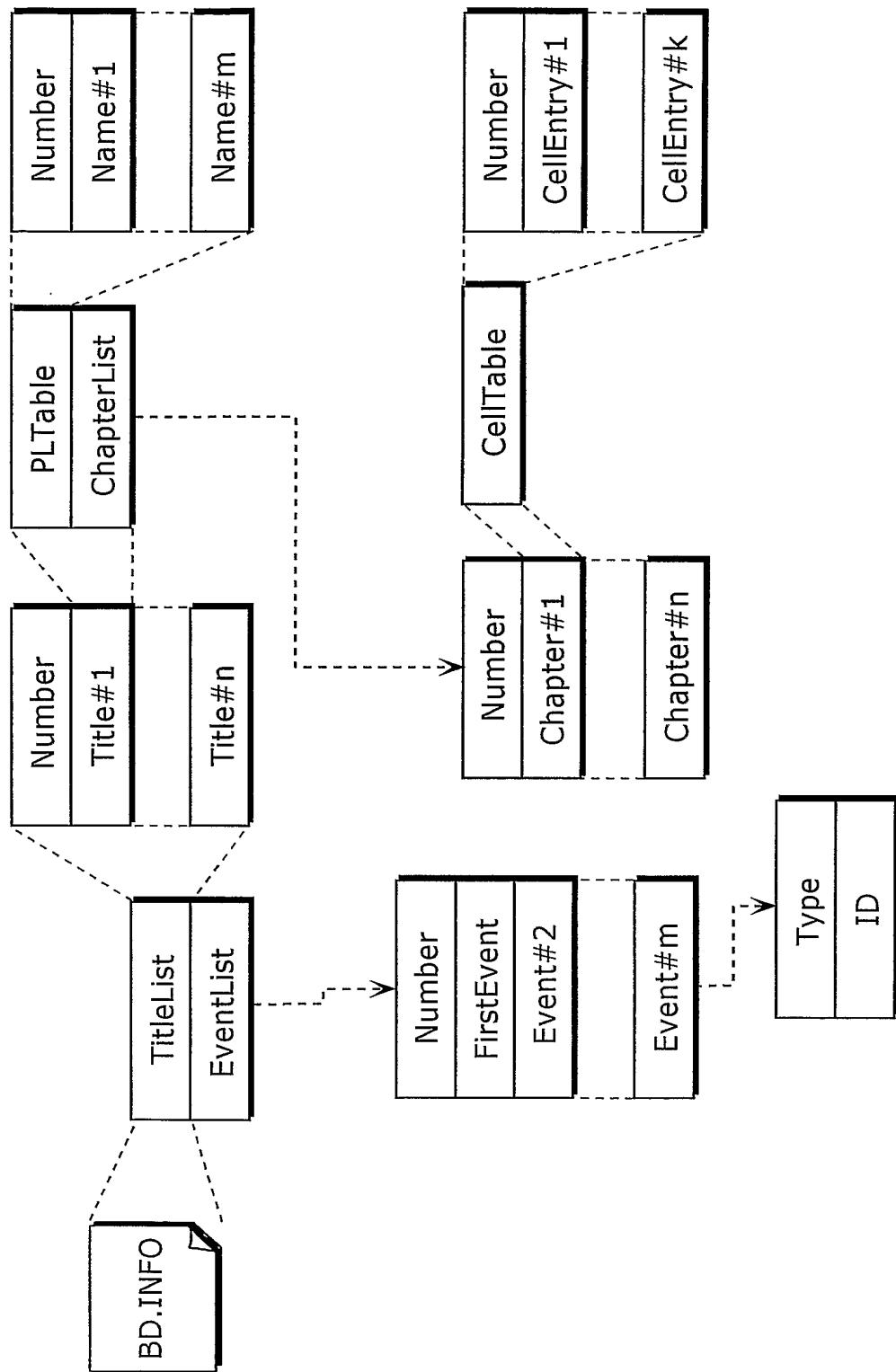


FIG. 29

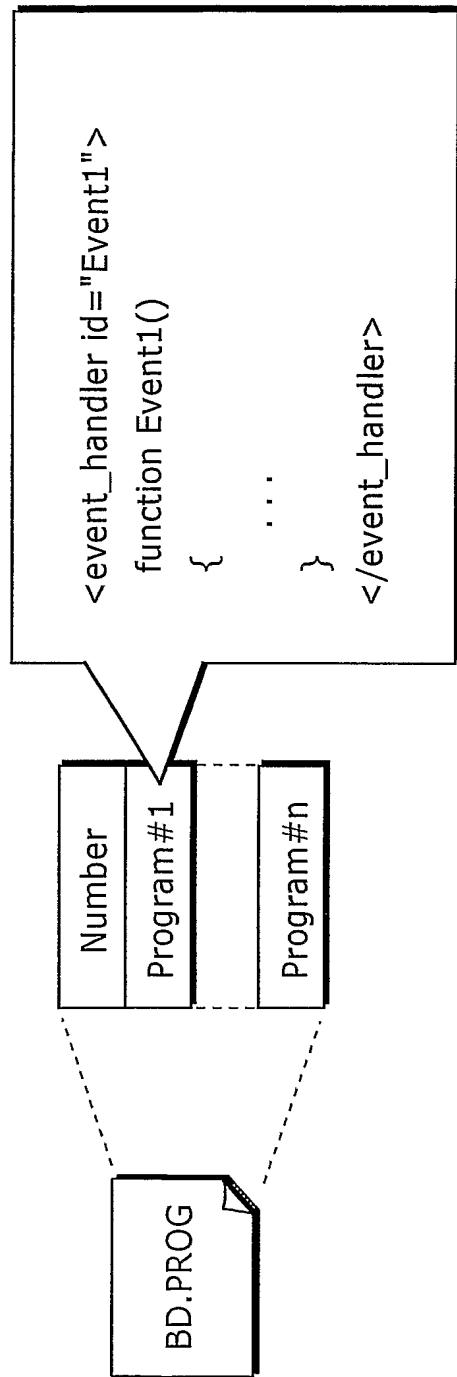


FIG. 30

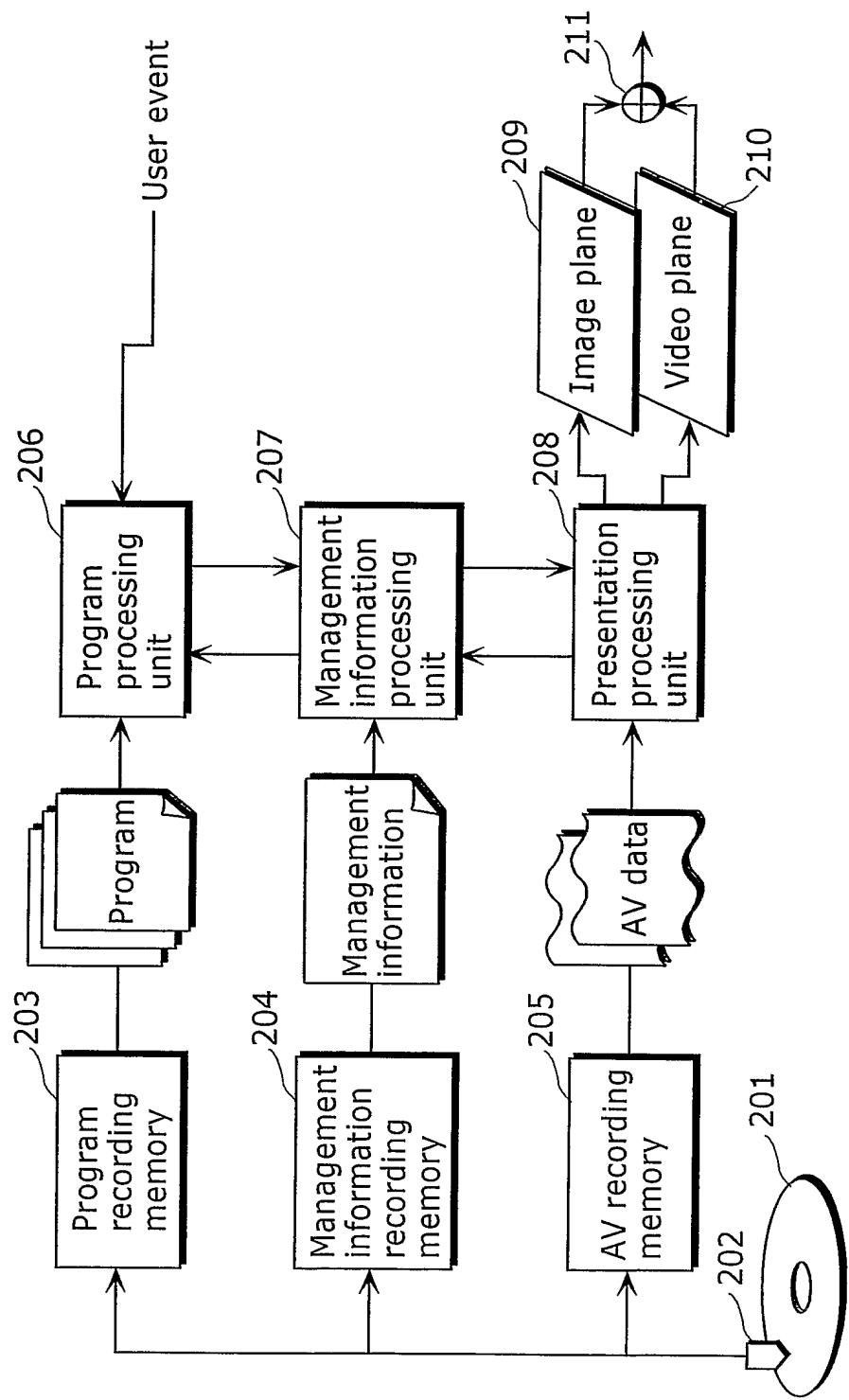


Fig. 31A

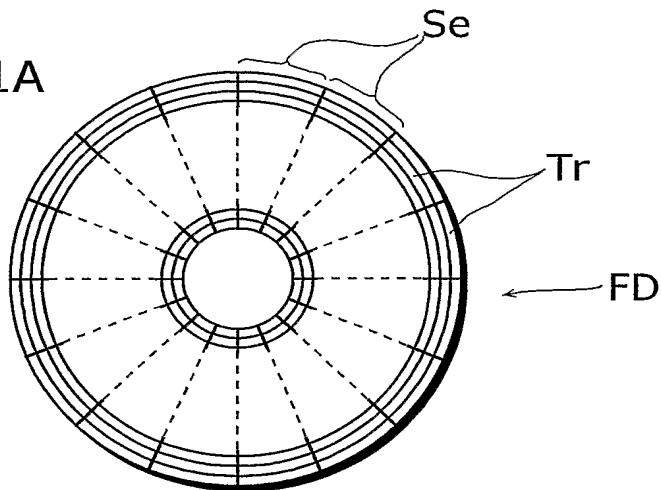


Fig. 31B

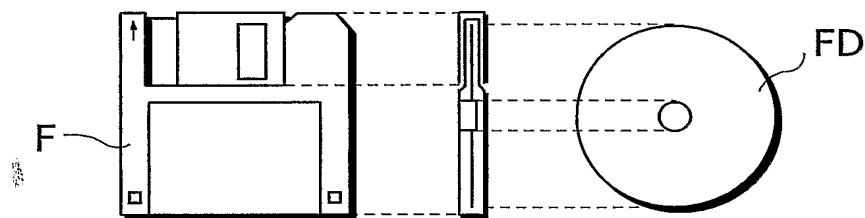


Fig. 31C

